

**CLAIM AMENDMENT**

Please **AMEND** claims 16-18, 21, 24, 26-29 and 34 as follows.

Please **ADD** new claims 39-68 as follows.

This listing of claims will replace all prior versions, and listings, of claims in the application:

1-15. (Cancelled)

16. (Currently amended) A method for driving a plasma display panel (PDP) comprising steps of:

initializing a state of cells for a predetermined period;

selectively discriminating cells to be turned on from cells not to be turned on; and

discharging the selected cells,

wherein a discharge space in a cell is discharged before the initializing step if the predetermined period for the initializing step is present between the discharging step for a preceding field and the initializing step.

17. (Currently amended) The method of claim 16, wherein discharging of the discharge space is induced in the cells discharged during the step of discharging the selected cells of the preceding field.

18. (Currently amended) A method for driving a plasma display panel in which successive field periods, each including a reset period for initializing the state of respective cells, an address period for selectively discriminating cells to be turned on from cells not to be turned

on and for performing an addressing operation, and a sustain period for discharging the addressed cells, are performed, and a reset stabilization period for inducing discharging in a discharge space in a cell is additionally performed before the reset period based on information in the preceding field period.

19. (Previously Presented) The method of claim 18, wherein, in the reset stabilization period, discharging is induced in the cells discharged in the sustain period of the preceding field.

20. (Previously Presented) The method of claim 18, wherein, in the reset stabilization period, at least one of the number of discharging occurrences, the width of discharge pulses, or the level of a discharge pulse voltage is varied depending on the information in the field period.

21. (Currently amended) A method for driving a plasma display panel in which successive field periods, each including a reset period for initializing the state of respective cells, an address period for selectively discriminating cells to be turned on from cells not to be turned on and for performing an addressing operation, and a sustain period for discharging the addressed cells, are performed, and a reset stabilization period for inducing discharging in a discharge space in a cell is additionally performed before the reset period based on a preceding period .

22. (Previously Presented) The method of claim 21, wherein, in the reset stabilization period, discharging is induced in the cells discharged in the sustain period of the preceding field.

23. (Previously Presented) The method of claim 21, wherein, in the reset stabilization period, at least one of the number of discharging occurrences, the width of discharge pulses, or the level of a discharge pulse voltage is varied depending on the immediately proceeding period.

24. (Currently amended) A method of driving a plasma display panel in which successive field periods, each including a reset period for initializing the state of respective cells, an address period for selectively discriminating cells to be turned on from cells not to be turned on and for performing an addressing operation, and a sustain period for discharging the addressed cells, are performed, and a rest period, which is determined by a difference between a frame period and a sum of the reset periods, the address periods and the sustain periods in the frame period, is positioned in the middle of the sustain period or the address period.

25. (Previously Presented) The method of claim 24, wherein the rest period is temporally divided and then distributed in the address period or the sustain period.

26. (Currently amended) A plasma display panel driving apparatus comprising:  
a reset signal generator for generating a reset signal initializing the state of respective cells;  
an address signal generator for generating an address signal selectively discriminating cells to be turned on from cells to be turned off and for performing an addressing operation; and  
a sustain signal generator for generating a sustain signal discharging the cells addressed by the address signal generator,

wherein if cell discharging does not occur for a time interval before application of the reset signal, the reset signal generator generates a reset stabilization signal to cause discharging to occur in the cells prior to the generation of the reset signal.

27. (Currently amended) A plasma display panel driving apparatus comprising:
- a reset signal generator for generating a reset signal initializing the state of respective cells;
  - an address signal generator for generating an address signal selectively discriminating cells to be turned on from cells to be turned off and for performing an addressing operation; and
  - a sustain signal generator for generating a sustain signal discharging the cells addressed by the address signal generator,
- wherein if a signal different from the sustain signal is applied for a time period before application of the reset signal, a reset stabilization signal generator generates a reset stabilization signal in the cells prior to the reset signal.

28. (Currently amended) A plasma display panel driving apparatus comprising:
- a reset signal generator for generating a reset signal initializing the state of respective cells in a reset period;
  - an address signal generator for generating an address signal selectively discriminating cells to be turned on from cells not to be turned on and for performing an addressing operation in an address period;

a sustain signal generator for generating a sustain signal discharging the cells addressed by the address signal generator in the sustain period; and

a signal synthesizer for applying the reset signal, the address signal, and the sustain signal to electrodes,

wherein if a rest period determined by a difference between a frame period and a sum of the reset periods, the address periods and the sustain periods in the frame period is present in a field consisting of the reset period, the address period, and the sustain period, the signal synthesizer synthesizes the reset signal, the address signal, and the sustain signal such that the rest period is positioned in the middle of the sustain period or the address period.

29. (Currently amended) A method of driving a plasma display panel in which a reset period for initializing the state of respective cells, an address period for selectively discriminating cells to be turned on from cells not to be turned on and for performing an addressing operation, a sustain period for discharging the addressed cells in the address period, and a reset stabilization period, if a rest period having a time duration follows the sustain period, for causing discharging in a discharge space in a cell before a next reset period, are performed, wherein the timing of the reset stabilization signal is based on an immediately proceeding period.

30. (Previously Presented) The method of claim 29, wherein, in the reset stabilization period, a predetermined number of reset pulses that are substantially the same as pulses applied to electrodes in the sustain period are applied.

31. (Previously Presented) The method of claim 29, wherein reset pulses applied in the reset period comprise a square pulse applied in an early stage of the reset period and a ramp pulse applied in a latter stage of the reset period with a gradually decreasing voltage level.

32. (Previously Presented) The method of claim 31, wherein, in the reset period, the reset pulses are applied to scan electrodes and a constant voltage is applied to sustain electrodes.

33. (Previously Presented) The method of claim 30, wherein reset pluses applied in the reset period comprise a first ramp pulse applied in an early stage of the reset period with a gradually increasing voltage level and a second ramp pulse applied in a latter stage of the reset period with a gradually decreasing voltage level.

34. (Currently amended) A method of driving a plasma display panel in which a reset period for initializing the state of respective cells, an address period for selectively discriminating cells to be turned on from cells not to be turned on and for performing an addressing operation, a sustain period for discharging the addressed cells in the address period, and a reset stabilization period, if a rest period having a time duration follows the sustain period, for causing discharging in a discharge space in a cell before a next reset period, are performed, wherein the timing of the reset stabilization signal is based on information in the field period.

35. (Previously Presented) The method of claim 34, wherein, in the reset stabilization period, a predetermined number of reset pulses that are substantially the same as pulses applied to electrodes in the sustain period are applied.

36. (Previously Presented) The method of claim 34, wherein reset pulses applied in the reset period comprise a square pulse applied in an early stage of the reset period and a ramp pulse applied in a latter stage of the reset period with a gradually decreasing voltage level.

37. (Previously Presented) The method of claim 36, wherein, in the reset period, the reset pulses are applied to scan electrodes and a constant voltage is applied to sustain electrodes.

38. (Previously Presented) The method of claim 33, wherein reset pluses applied in the reset period comprise a first ramp pulse applied in an early stage of the reset period with a gradually increasing voltage level and a second ramp pulse applied in a latter stage of the reset period with a gradually decreasing voltage level.

39. (New) The apparatus of claim 26, wherein the timing of the reset stabilization signal is based on a preceding period.

40. (New) The apparatus of claim 27, wherein the timing of the reset stabilization signal is based on information in the time period.

41. (New) The apparatus of claim 27, wherein the reset stabilization signal generator is the same as the reset signal generator.

42. (New) The method of claim 20, wherein the information is the duration of a rest period in which cell discharging does not occur.

43. (New) A method for driving a plasma display panel in which successive field periods, each including a reset period applying a reset signal for initializing the state of respective cells, an address period applying an address signal for selectively discriminating cells to be turned on from cells not to be turned on and for performing an addressing operation, and a sustain period applying a sustain signal for discharging the addressed cells, are performed, and a reset stabilization period applying a reset stabilization signal having waveform different from the waveform of the sustain signal for inducing discharging in a discharge space is additionally performed before the reset period.

44. (New) The method of claim 43, wherein the reset stabilization period is based on information in preceding field period.

45. (New) The method of claim 43, wherein the voltage level of the reset stabilization signal is different from the voltage level of the sustain signal.

46. (New) The method of claim 45, wherein the voltage level of the reset stabilization signal is higher than that of the sustain signal.

47. (New) The method of claim 45, wherein the polarity of the reset stabilization signal is equal to the polarity of the sustain signal.



48. (New) The method of claim 43, wherein the pulse width of the reset stabilization signal is different from the pulse width of the sustain signal.

49. (New) The method of claim 48, wherein the pulse width of the reset stabilization signal is wider than that of the sustain signal.

50. (New) The method of claim 47, wherein a rest period in which no discharge in the cells occurs for a predetermined time and the reset stabilization period are positioned in this order between the sustain period of preceding field and the reset period.

51. (New) The method of claim 48, wherein the reset stabilization period is positioned between the sustain period of preceding field and the reset period.

52. (New) A plasma display panel driving apparatus comprising:  
a reset signal generator for generating a reset signal initializing the state of respective cells in a reset period;  
an address signal generator for generating an address signal selectively discriminating cells to be turned on from cells to be turned off and for performing an addressing operation in an address period; and  
a sustain signal generator for generating a sustain signal discharging the cells addressed by the address signal generator in a sustain period,

wherein if cell discharging does not occur for a time interval before application of the reset signal, a reset stabilization signal generator generates a reset stabilization signal having the waveform different from the waveform of the sustain signal to cause discharging to occur in the cells prior to the reset signal.

53. (New) The apparatus of claim 52, wherein the timing of the reset stabilization signal is based on information in the time interval.

54. (New) The apparatus of claim 52, wherein the reset stabilization signal generator is the same as the reset signal generator.

55. (New) The apparatus of claim 52, wherein the reset stabilization signal generator generates the reset stabilization signal having the voltage level different from the voltage level of the sustain signal generated from the sustain signal generator.

56. (New) The apparatus of claim 55, wherein the reset stabilization signal generator generates the reset stabilization signal having the voltage level higher than that of the sustain signal generated from the sustain signal generator.

57. (New) The apparatus of claim 55, wherein the reset stabilization signal generator generates the reset stabilization signal having the polarity equal to the polarity of the sustain signal generated from the sustain signal generator.

58. (New) The apparatus of claim 52, wherein the reset stabilization signal generator generates the reset stabilization signal having the pulse width different from the pulse width of the sustain signal generated from the sustain signal generator.

59. (New) The apparatus of claim 58, wherein the reset stabilization signal generator generates the reset stabilization signal having the pulse width wider than that of the sustain signal generated from the sustain signal generator.

60. (New) A plasma display panel driving apparatus comprising:  
a reset signal generator for generating a reset signal initializing the state of respective cells in a reset period;  
an address signal generator for generating an address signal selectively discriminating cells to be turned on from cells to be turned off and for performing an addressing operation in an address period;  
a sustain signal generator for generating a sustain signal discharging the cells addressed by the address signal generator in a sustain period; and  
a signal synthesizing circuit for synthesizing the signals and for applying the signals to each electrode and for positioning a rest period, in which a signal which is different from the sustain signal is applied, between the periods,  
wherein a reset stabilization signal generator generates a reset stabilization signal to cause discharging in the cells before application of the reset signal.

61. (New) The apparatus of claim 60, wherein the reset stabilization signal generator is the same as the reset signal generator.

62. (New) The apparatus of claim 60, wherein the reset stabilization signal generator is the same as the sustain signal generator.

63. (New) The apparatus of claim 60, wherein the reset stabilization signal generator generates the reset stabilization signal having the waveform different from the waveform of the sustain signal generated from the sustain signal generator.

64. (New) The apparatus of claim 63, wherein the reset stabilization signal generator generates the reset stabilization signal having the voltage level different from the voltage level of the sustain signal generated from the sustain signal generator.

65. (New) The apparatus of claim 64, wherein the reset stabilization signal generator generates the reset stabilization signal having the voltage level higher than that of the sustain signal generated from the sustain signal generator.

66. (New) The apparatus of claim 64, wherein the reset stabilization signal generator generates the reset stabilization signal having the polarity equal to the polarity of the sustain signal generated from the sustain signal generator.

67. (New) The apparatus of claim 63, wherein the reset stabilization signal generator generates the reset stabilization signal having the pulse width different from the pulse width of the sustain signal generated from the sustain signal generator.

68. (New) The apparatus of claim 67, wherein the reset stabilization signal generator generates the reset stabilization signal having the pulse width wider than that of the sustain signal generated from the sustain signal generator.